

TOYOTA FLD 19Fver3 - Task #1737

Task # 1610 (进行中): FLD System Architecture Design

Task # 1612 (进行中): FLD_SWEA: Software Achitecture Design

Task # 1710 (进行中): FLD_SWEA: Software Achitecture Design - Software Components

FLD_SWEA: Software Achitecture Design - Software Low Layer Components

2023-10-25 18:31 - 德乐 秦

状态:	进行中	开始日期:	2023-11-19
优先级:	普通	计划完成日期:	2023-11-27
指派给:	德乐 秦	% 完成:	0%
类别:		预期时间:	0.00 小时
目标版本:	FLD_H000.1_A001.00.00	耗时:	0.00 小时
描述			
子任务:			
Task # 1738: FLD_SWEA: Software Achitecture Design - MCU Clock			进行中
Task # 1739: FLD_SWEA: Software Achitecture Design - MCU Port			进行中
Task # 1740: FLD_SWEA: Software Achitecture Design - MCU GPIO			进行中
Task # 1741: FLD_SWEA: Software Achitecture Design - MCU ADC			进行中
Task # 1742: FLD_SWEA: Software Achitecture Design - MCU PWM			进行中
Task # 1743: FLD_SWEA: Software Achitecture Design - MCU SPI			进行中
Task # 1744: FLD_SWEA: Software Achitecture Design - MCU UART			进行中
Task # 1745: FLD_SWEA: Software Achitecture Design - MCU Watc...			进行中
Task # 1746: FLD_SWEA: Software Achitecture Design - MCU Timer			进行中
Task # 1747: FLD_SWEA: Software Achitecture Design - MCU Work...			进行中
Task # 1748: FLD_SWEA: Software Achitecture Design - MCU Inte...			进行中
Task # 1749: FLD_SWEA: Software Achitecture Design - MCU Flash			进行中
Task # 1750: FLD_SWEA: Software Achitecture Design - MCU EEPROM			进行中
Task # 1751: FLD_SWEA: Software Achitecture Design - MCU DMA			进行中

历史记录

#1 - 2023-10-25 18:31 - 德乐 秦

MCAL configuration and debugging