

TOYOTA FLD 19Fver3 - Task #1619

Task # 1616 (进行中): FLD Detail Design

FLD_HWED: Hardware Detail Design

2023-10-24 19:02 - 斌 徐

状态:	进行中	开始日期:	2023-10-31
优先级:	普通	计划完成日期:	2024-01-05
指派给:	平 陈	% 完成:	0%
类别:		预期时间:	0.00 小时
目标版本:	FLD_H000.1_A001.00.00	耗时:	0.00 小时
描述			
子任务:			
Task # 1647: FLD_HWED : Hardware schmatic design - Input Circuits			进行中
Task # 1637: FLD_HWED : Hardware schmatic design - Power input			进行中
Task # 1638: FLD_HWED : Hardware schmatic design - CAN SBC			进行中
Task # 1639: FLD_HWED : Hardware schmatic design - TNS Signal de...			进行中
Task # 1641: FLD_HWED : Hardware schmatic design - RLD1 and RLD2			进行中
Task # 1642: FLD_HWED : Hardware schmatic design - HEDL HLC			进行中
Task # 1643: FLD_HWED : Hardware schmatic design - RBIN NTC			进行中
Task # 1644: FLD_HWED : Hardware schmatic design - Left Right de...			进行中
Task # 1645: FLD_HWED : Hardware schmatic design - PWM input Det...			进行中
Task # 1646: FLD_HWED : Hardware schmatic design - TNLS Hard			进行中
Task # 1648: FLD_HWED : Hardware schmatic design - Power Topology			进行中
Task # 1649: FLD_HWED : Hardware schmatic design - Boost			进行中
Task # 1650: FLD_HWED : Hardware schmatic design - Buck			进行中
Task # 1651: FLD_HWED : Hardware schmatic design - HSSS			进行中
Task # 1652: FLD_HWED : Hardware schmatic design - LSSS			进行中
Task # 1653: FLD_HWED : Hardware schmatic design - Enviroment Si...			进行中
Task # 1654: FLD_HWED : Hardware schmatic design - LB signal det...			进行中
Task # 1655: FLD_HWED : Hardware schmatic design - HSSS/LSSS Sig...			进行中
Task # 1656: FLD_HWED : Hardware schmatic design - Power Signals...			进行中
Task # 1657: FLD_HWED : Hardware schmatic design - Buck output v...			进行中
Task # 1658: FLD_HWED : Hardware schmatic design - board tempera...			进行中
Task # 1659: FLD_HWED : Hardware schmatic design - Communication			进行中
Task # 1660: FLD_HWED : Hardware schmatic design - LIN Communica...			进行中
Task # 1661: FLD_HWED : Hardware schmatic design - UART CAN Comm...			进行中
Task # 1662: FLD_HWED : Hardware schmatic design - CAN FD Commun...			进行中
Task # 1663: FLD_HWED : Hardware schmatic design - Output Circuits			进行中
Task # 1664: FLD_HWED : Hardware schmatic design - FAN			进行中
Task # 1665: FLD_HWED : Hardware schmatic design - DAC			进行中
Task # 1666: FLD_HWED : Hardware schmatic design - Reserved buck...			进行中
Task # 1667: FLD_HWED : Hardware schmatic design - TRSP Control			进行中
Task # 1668: FLD_HWED : Hardware schmatic design - MCU Topology			进行中
Task # 1669: FLD_HWED : Hardware schmatic design - GPIO and ADC			进行中
Task # 1670: FLD_HWED : Hardware schmatic design - PWM			进行中
Task # 1671: FLD_HWED : Hardware schmatic design - SPI			进行中
Task # 1672: FLD_HWED: Hardware schematic Review			进行中
Task # 1673: FLD_HWED: Hardware schematic upgrade			进行中
Task # 1674: FLD_HWED: Prepare hardware interface documents			进行中
Task # 1675: FLD_HWED: PCB Layout			进行中

Task # 1676: FLD_HWED: PCB Layout Review	进行中
Task # 1677: FLD_HWED: PCB Layout Upgrade	进行中
Task # 1678: FLD_HWED: PCB Manufacture	进行中
Task # 1679: FLD_HWED: A sample hardware module prepare	进行中
Task # 1680: FLD_HWED: Prepare connector and wire harness	进行中
Task # 1681: FLD_HWED: Prepare board test case	进行中
Task # 1682: FLD_HWED: Hardware Board Test	进行中
Task # 1683: FLD_HWED: Hardware and Software debugging	进行中

历史记录

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- 目标版本 被设置为 FLD_H000.1_A001.00.00