

TOYOTA FLD 19Fver3 - Task #1616

FLD Detail Design

2023-10-24 17:39 - 斌 徐

状态:	进行中	开始日期:	2023-09-01
优先级:	普通	计划完成日期:	2024-01-24
指派给:	慧浩 川端	% 完成:	0%
类别:		预期时间:	0.00 小时
目标版本:	FLD_H000.1_A001.00.00	耗时:	0.00 小时
描述			
Follow up project progress			
子任务:			
Task # 1619: FLD_HWED: Hardware Detail Design			进行中
Task # 1647: FLD_HWED : Hardware schmatic design - Input Circuits			进行中
Task # 1637: FLD_HWED : Hardware schmatic design - Power input			进行中
Task # 1638: FLD_HWED : Hardware schmatic design - CAN SBC			进行中
Task # 1639: FLD_HWED : Hardware schmatic design - TNS Signal de...			进行中
Task # 1641: FLD_HWED : Hardware schmatic design - RLD1 and RLD2			进行中
Task # 1642: FLD_HWED : Hardware schmatic design - HEDL HLC			进行中
Task # 1643: FLD_HWED : Hardware schmatic design - RBIN NTC			进行中
Task # 1644: FLD_HWED : Hardware schmatic design - Left Right de...			进行中
Task # 1645: FLD_HWED : Hardware schmatic design - PWM input Det...			进行中
Task # 1646: FLD_HWED : Hardware schmatic design - TNLS Hard			进行中
Task # 1648: FLD_HWED : Hardware schmatic design - Power Topology			进行中
Task # 1649: FLD_HWED : Hardware schmatic design - Boost			进行中
Task # 1650: FLD_HWED : Hardware schmatic design - Buck			进行中
Task # 1651: FLD_HWED : Hardware schmatic design - HSSS			进行中
Task # 1652: FLD_HWED : Hardware schmatic design - LSSS			进行中
Task # 1653: FLD_HWED : Hardware schmatic design - Enviroment Si...			进行中
Task # 1654: FLD_HWED : Hardware schmatic design - LB signal det...			进行中
Task # 1655: FLD_HWED : Hardware schmatic design - HSSS/LSSS Sig...			进行中
Task # 1656: FLD_HWED : Hardware schmatic design - Power Signals...			进行中
Task # 1657: FLD_HWED : Hardware schmatic design - Buck output v...			进行中
Task # 1658: FLD_HWED : Hardware schmatic design - board tempera...			进行中
Task # 1659: FLD_HWED : Hardware schmatic design - Communication			进行中
Task # 1660: FLD_HWED : Hardware schmatic design - LIN Communica...			进行中
Task # 1661: FLD_HWED : Hardware schmatic design - UART CAN Comm...			进行中
Task # 1662: FLD_HWED : Hardware schmatic design - CAN FD Commun...			进行中
Task # 1663: FLD_HWED : Hardware schmatic design - Output Circuits			进行中
Task # 1664: FLD_HWED : Hardware schmatic design - FAN			进行中
Task # 1665: FLD_HWED : Hardware schmatic design - DAC			进行中
Task # 1666: FLD_HWED : Hardware schmatic design - Reserved buck...			进行中
Task # 1667: FLD_HWED : Hardware schmatic design - TRSP Control			进行中
Task # 1668: FLD_HWED : Hardware schmatic design - MCU Topology			进行中
Task # 1669: FLD_HWED : Hardware schmatic design - GPIO and ADC			进行中
Task # 1670: FLD_HWED : Hardware schmatic design - PWM			进行中
Task # 1671: FLD_HWED : Hardware schmatic design - SPI			进行中
Task # 1672: FLD_HWED: Hardware schematic Review			进行中
Task # 1673: FLD_HWED: Hardware schematic upgrade			进行中
Task # 1674: FLD_HWED: Prepare hardware interface documents			进行中
Task # 1675: FLD_HWED: PCB Layout			进行中

Task # 1676: FLD_HWED: PCB Layout Review	进行中
Task # 1677: FLD_HWED: PCB Layout Upgrade	进行中
Task # 1678: FLD_HWED: PCB Manufacture	进行中
Task # 1679: FLD_HWED: A sample hardware module prepare	进行中
Task # 1680: FLD_HWED: Prepare connector and wire harness	进行中
Task # 1681: FLD_HWED: Prepare board test case	进行中
Task # 1682: FLD_HWED: Hardware Board Test	进行中
Task # 1683: FLD_HWED: Hardware and Software debugging	进行中
Task # 1620: FLD_SWED: Software Detail Design	进行中
Task # 1755: FLD_SWED: Software Detail Design - Softwre Applic...	进行中
Task # 1756: FLD_SWED: Software Detail Design - Input Signal P...	进行中
Task # 1757: FLD_SWED: Software Detail Design - Derating	进行中
Task # 1758: FLD_SWED: Software Detail Design - Lighting logic...	进行中
Task # 1759: FLD_SWED: Software Detail Design - Fault Monitor	进行中
Task # 1760: FLD_SWED: Software Detail Design - Fault Action	进行中
Task # 1761: FLD_SWED: Software Detail Design - System Management	进行中
Task # 1762: FLD_SWED: Software Detail Design - Configuration ...	进行中
Task # 1763: FLD_SWED: Software Detail Design - Test Api	进行中
Task # 1764: FLD_SWED: Software Detail Design - Run Time Debug...	进行中
Task # 1765: FLD_SWED: Software Detail Design - application in...	进行中
Task # 1766: FLD_SWED: Software Detail Design - application co...	进行中
Task # 1767: FLD_SWED: Software Detail Design - Softwre Middle...	进行中
Task # 1768: FLD_SWED: Software Detail Design - CAN FD Stack	进行中
Task # 1769: FLD_SWED: Software Detail Design - LIN Stack	进行中
Task # 1770: FLD_SWED: Software Detail Design - NTC derating	进行中
Task # 1771: FLD_SWED: Software Detail Design - Board protection	进行中
Task # 1772: FLD_SWED: Software Detail Design - Animation	进行中
Task # 1773: FLD_SWED: Software Detail Design - EEPROM abstrac...	进行中
Task # 1775: FLD_SWED: Software Detail Design - Boost Driver	进行中
Task # 1776: FLD_SWED: Software Detail Design - Buck Driver	进行中
Task # 1777: FLD_SWED: Software Detail Design - Matrix Driver	进行中
Task # 1778: FLD_SWED: Software Detail Design - HSSS LSSS Driver	进行中
Task # 1779: FLD_SWED: Software Detail Design - TRSP Driver	进行中
Task # 1780: FLD_SWED: Software Detail Design - Motor (LIN) Dr...	进行中
Task # 1781: FLD_SWED: Software Detail Design - Motor (DAC) Dr...	进行中
Task # 1782: FLD_SWED: Software Detail Design - FAN Driver	进行中
Task # 1783: FLD_SWED: Software Detail Design - Enviroment sig...	进行中
Task # 1784: FLD_SWED: Software Detail Design - Software Middl...	进行中
Task # 1785: FLD_SWED: Software Detail Design - Software Middl...	进行中
Task # 1786: FLD_SWED: Software Detail Design - Software Middl...	进行中
Task # 1787: FLD_SWED: Software Detail Design - Software Low L...	进行中
Task # 1788: FLD_SWED: Software Detail Design - MCU clock	进行中
Task # 1789: FLD_SWED: Software Detail Design - MCU Port	进行中
Task # 1790: FLD_SWED: Software Detail Design - MCU GPIO	进行中
Task # 1791: FLD_SWED: Software Detail Design - MCU ADC	进行中
Task # 1792: FLD_SWED: Software Detail Design - MCU PWM	进行中
Task # 1793: FLD_SWED: Software Detail Design - MCU SPI	进行中
Task # 1794: FLD_SWED: Software Detail Design - MCU UART	进行中
Task # 1795: FLD_SWED: Software Detail Design - MCU Watch dog	进行中
Task # 1796: FLD_SWED: Software Detail Design - MCU Timer	进行中
Task # 1797: FLD_SWED: Software Detail Design - MCU working mode	进行中
Task # 1798: FLD_SWED: Software Detail Design - MCU Interrupts	进行中
Task # 1799: FLD_SWED: Software Detail Design - MCU Flash	进行中

Task # 1800: FLD_SWED: Software Detail Design - MCU EEPROM	进行中
Task # 1801: FLD_SWED: Software Detail Design - MCU DMA	进行中
Task # 1802: FLD_SWED: Software Detail Design - Software Low L...	进行中
Task # 1803: FLD_SWED: Software Detail Design - Software Low L...	进行中
Task # 1804: FLD_SWED: Software Detail Design - Software Low L...	进行中
Task # 1805: FLD_SWEI: Software Intergration Test - Software I...	进行中
Task # 1806: FLD_SWEI: Software Intergration Test - MCU (Debug...	进行中
Task # 1807: FLD_SWEI: Software Intergration Test - MCU (Debug...	进行中
Task # 1808: FLD_SWEI: Software Intergration Test - Merge midd...	进行中
Task # 1809: FLD_SWEI: Software Intergration Test - Merge appl...	进行中
Task # 1810: FLD_SWEI: Software Intergration Test - Debugging ...	进行中
Task # 1621: FLD_MECD: Mechanical Detail Design	进行中
Task # 1622: FLD_SWEU: Software Unit Test	进行中
Task # 1623: FLD_SWEI: Software Intergration Test	进行中

历史记录

#1 - 2023-10-24 17:40 - 斌徐

- 计划完成日期 被设置为 2023-11-13
- 开始日期 从 2023-10-24 变更为 2023-09-01

#2 - 2023-10-24 19:19 - 斌徐

- 目标版本 被设置为 FLD_H000.1_A001.00.00