

TOYOTA FLD 19Fver3 - Task #1612

Task # 1610 (进行中): FLD System Architecture Design

FLD\_SWEA: Software Achitecture Design

2023-10-24 17:10 - 斌 徐

|                                                                  |                       |         |            |
|------------------------------------------------------------------|-----------------------|---------|------------|
| 状态:                                                              | 进行中                   | 开始日期:   | 2023-11-15 |
| 优先级:                                                             | 普通                    | 计划完成日期: | 2023-12-09 |
| 指派给:                                                             | 德乐 秦                  | % 完成:   | 0%         |
| 类别:                                                              |                       | 预期时间:   | 0.00 小时    |
| 目标版本:                                                            | FLD_H000.1_A001.00.00 | 耗时:     | 0.00 小时    |
| 描述                                                               |                       |         |            |
| 子任务:                                                             |                       |         |            |
| Task # 1709: FLD_SWEA: Software Achitecture Design - Block Di... |                       |         | 进行中        |
| Task # 1710: FLD_SWEA: Software Achitecture Design - Software... |                       |         | 进行中        |
| Task # 1711: FLD_SWEA: Software Achitecture Design - Software... |                       |         | 进行中        |
| Task # 1712: FLD_SWEA: Software Achitecture Design - Inputs S... |                       |         | 进行中        |
| Task # 1713: FLD_SWEA: Software Achitecture Design - Detating    |                       |         | 进行中        |
| Task # 1714: FLD_SWEA: Software Achitecture Design - lighting... |                       |         | 进行中        |
| Task # 1715: FLD_SWEA: Software Achitecture Design - Fault Mo... |                       |         | 进行中        |
| Task # 1716: FLD_SWEA: Software Achitecture Design - Fault Ac... |                       |         | 进行中        |
| Task # 1717: FLD_SWEA: Software Achitecture Design - System M... |                       |         | 进行中        |
| Task # 1718: FLD_SWEA: Software Achitecture Design - Configur... |                       |         | 进行中        |
| Task # 1719: FLD_SWEA: Software Achitecture Design - Test API    |                       |         | 进行中        |
| Task # 1720: FLD_SWEA: Software Achitecture Design - Software... |                       |         | 进行中        |
| Task # 1721: FLD_SWEA: Software Achitecture Design - CAN FD s... |                       |         | 进行中        |
| Task # 1722: FLD_SWEA: Software Achitecture Design - LIN stack   |                       |         | 进行中        |
| Task # 1723: FLD_SWEA: Software Achitecture Design - NTC dera... |                       |         | 进行中        |
| Task # 1724: FLD_SWEA: Software Achitecture Design - RBIN        |                       |         | 进行中        |
| Task # 1725: FLD_SWEA: Software Achitecture Design - Board Pr... |                       |         | 进行中        |
| Task # 1726: FLD_SWEA: Software Achitecture Design - Animation   |                       |         | 进行中        |
| Task # 1728: FLD_SWEA: Software Achitecture Design - Boost Dr... |                       |         | 进行中        |
| Task # 1729: FLD_SWEA: Software Achitecture Design - Buck Driver |                       |         | 进行中        |
| Task # 1730: FLD_SWEA: Software Achitecture Design - matrix D... |                       |         | 进行中        |
| Task # 1731: FLD_SWEA: Software Achitecture Design - HSSS LSS... |                       |         | 进行中        |
| Task # 1732: FLD_SWEA: Software Achitecture Design - TRSP Driver |                       |         | 进行中        |
| Task # 1733: FLD_SWEA: Software Achitecture Design - Motor (L... |                       |         | 进行中        |
| Task # 1734: FLD_SWEA: Software Achitecture Design - Motor (D... |                       |         | 进行中        |
| Task # 1735: FLD_SWEA: Software Achitecture Design - FAN Driver  |                       |         | 进行中        |
| Task # 1736: FLD_SWEA: Software Achitecture Design - Envirome... |                       |         | 进行中        |
| Task # 1774: FLD_SWEA: Software Achitecture Design - EEPROM a... |                       |         | 进行中        |
| Task # 1737: FLD_SWEA: Software Achitecture Design - Software... |                       |         | 进行中        |
| Task # 1738: FLD_SWEA: Software Achitecture Design - MCU Clock   |                       |         | 进行中        |
| Task # 1739: FLD_SWEA: Software Achitecture Design - MCU Port    |                       |         | 进行中        |
| Task # 1740: FLD_SWEA: Software Achitecture Design - MCU GPIO    |                       |         | 进行中        |
| Task # 1741: FLD_SWEA: Software Achitecture Design - MCU ADC     |                       |         | 进行中        |
| Task # 1742: FLD_SWEA: Software Achitecture Design - MCU PWM     |                       |         | 进行中        |
| Task # 1743: FLD_SWEA: Software Achitecture Design - MCU SPI     |                       |         | 进行中        |
| Task # 1744: FLD_SWEA: Software Achitecture Design - MCU UART    |                       |         | 进行中        |
| Task # 1745: FLD_SWEA: Software Achitecture Design - MCU Watc... |                       |         | 进行中        |
| Task # 1746: FLD_SWEA: Software Achitecture Design - MCU Timer   |                       |         | 进行中        |

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| Task # 1747: FLD_SWEA: Software Achitecture Design - MCU Work... | 进行中 |
| Task # 1748: FLD_SWEA: Software Achitecture Design - MCU Inte... | 进行中 |
| Task # 1749: FLD_SWEA: Software Achitecture Design - MCU Flash   | 进行中 |
| Task # 1750: FLD_SWEA: Software Achitecture Design - MCU EEPROM  | 进行中 |
| Task # 1751: FLD_SWEA: Software Achitecture Design - MCU DMA     | 进行中 |
| Task # 1752: FLD_SWEA: Software Achitecture Design - Boot loader | 进行中 |
| Task # 1753: FLD_SWEA: Software Achitecture Design - Diagnost... | 进行中 |
| Task # 1754: FLD_SWEA: Software Achitecture Design - DFEMA       | 进行中 |

### 历史记录

- #1 - 2023-10-24 17:20 - 斌 徐
- 主题 从 FLD\_SYSA: Software Achitecture Design 变更为 FLD\_SWEA: Software Achitecture Design
- #2 - 2023-10-24 19:18 - 斌 徐
- 目标版本 被设置为 FLD\_H000.1\_A001.00.00